## PATENT COOPERATION TREATY

# TRANSLATION INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

(Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference  JP 920030200				FOR FURTHER A	ACTION	See Form PCT/IPEA/416					
International application No.				International filing da	ate (day/month/year)	Priority date (day/month/year)					
PCT/JP2004/016296				04.11.200	4	06.11.2003					
International Patent Classification (IPC) or national classification and IPC											
G11C11/407											
Applicant											
INTERNATIONAL BUSINESS MACHINES CORPORATION											
This report is the international preliminary examination report, established by this International Preliminary Examining Authority											
				ne applicant according t							
2.	This REPORT consists of a total of sheets, including this cover sheet.										
3.	This rep	ort is als	o accompanied by A	NNEXES, comprising	:						
	a. [	l (sent	to the applicant and	to the International Bi	ureau) a total of	sheets, as follows:					
					•	mended and are the basis for this report and/or					
						le 70.16 and Section 607 of the Administrative					
	sheets which supersede earlier sheets, but which this Authority considers contain an amendment that goes beyond the disclosure in the international application as filed, as indicated in item 4 of Box No. I and the Supplemental Box.										
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	, containing a sequence listing and/or tables related thereto, in computer readable form only, as indicated in the Supplemental Box Relating to Sequence Listing (see Section 802 of the Administrative Instructions).										
4.											
	$\boxtimes$	Box No.	I Basis of the	report							
		Box No.		•							
					h regard to novelty, invent	ive step and industrial applicability					
	$\overline{\Box}$	Box No.		•							
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		Box No.		d explanations support		,					
	$\sqcup$	Box No.	VI Certain doc	uments cited							
	Box No. VII Certain defects in the international application										
	Box No. VIII Certain observations on the international application										
Date of s	ubmissio	on of the	demand		Date of completion of the	is report .					
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Name and mailing address of the IPEA/JP					Authorized officer						
Facsimile	e No				Telephone No						

Form PCT/IPEA/409 (cover sheet) (January 2004)

### INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.
PCT/JP2004/016296

Box	No. I Basis of the report
1.	With regard to the language, this report is based on the international application in the language in which it was filed, unless otherwise indicated under this item.
	This report is based on translations from the original language into the following
	which is the language of a translation furnished for the purposes of:
	international search (Rule 12.3 and 23.1(b))
	publication of the international application (Rule 12.4)
	international preliminary examination (Rule 55.2 and/or 55.3)
2.	With regard to the elements of the international application, this report is based on (replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to this report):
	the international application as originally filed/furnished
	the description:
	pages as originally filed/furnished
	pages* received by this Authority on
	pages* received by this Authority on
	the claims:
	nosas originally filed/furnished
	nos.* as amended (together with any statement) under Article 19
	nos.* received by this Authority on
	nos.* received by this Authority on
	the drawings:
	sheets as originally filed/furnished
	sheets* received by this Authority on
	sheets* received by this Authority on
	a sequence listing and/or any related table(s) – see Supplemental Box Relating to Sequence Listing.
}	
3.	The amendments have resulted in the cancellation of:
	Lighther description, pages
	the claims, nos.
	the drawings, sheets/figs
	the sequence listing (specify):
١.	any table(s) related to sequence listing (specify):
4.	This report has been established as if (some of) the amendments annexed to this report and listed below had not been made, since they have been considered to go beyond the disclosure as filed, as indicated in the Supplemental Box (Rule 70.2(c)).
	the description, pages
	the claims, nos.
	the drawings, sheets/figs
	the sequence listing (specify):
	any table(s) related to sequence listing (specify):
	If item 4 applies, some or all of those sheets may be marked "superseded."

#### INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.
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Box		Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement					
1.	Statement						
	Novelty (N) Claim	1-6	YES				
	Claim	us	NO				
	Inventive step (IS) Claim	is .	YES				
	Claim	1-6	_ NO				
	Industrial applicability (IA) Claim	1-6	YES				
	Clain		NO				
	Citation and analysis of (Bula 70.7)						
2.	Citations and explanations (Rule 70.7)	202700 7 (NDC Company) 27 July					
		-202780 A (NEC Corporation), 27 July					
		paragraphs [0013] to [0015]; fig. 4					
		& US 2001/0008282 A1 & KR 2001/100765					
	A						
		1-260181 A (Toshiba Corporation), 22					
	<del>-</del>	ber 2000, entire text; all drawings &					
	US 616	5301 A & EP 1035548 A1 & KR					
	2000/0	62766 A					
		-195253 A (International Business					
	Machin	es Corporation), 14 July 2000, entire					
	text;	all drawings & US 6252794 B1 & KR					
	2000/0	47637 A					
	Document 4: JP 8-22	1978 A (Hitachi, Ltd.), 30 August					
	1996,	entire text; all drawings (Family:					
	none)						
	•						
	[Claims 1 and 4]						
	The inventions set forth in claims 1 and 4 do not						
	involve an inventive step in the light of documents $1$ , $2$						
	and 3 cited in the international search report.						
	Document 1 sets forth a multi-bit pre-fetch type						
	semiconductor storage device, provided with a means of						
	controlling a colum	nn decoder in such a manner that a					

Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

plurality of column selection lines YSW1 and YSW2 are driven in sequence during activation of a sense amplifier.

The plurality of column selection lines YSW1 and YSW2 in document 1 are provided in a manner so as to correspond to a plurality of memory cell arrays, and although not provided in a manner so as to correspond to groups into which a plurality of switches have been divided, it is a known technique, as set forth in document 2, for example, to provide a plurality of bit switches divided up into a plurality of groups between a plurality of latch circuits and a plurality of bit line pairs in a multi-bit pre-fetch semiconductor storage device, and it would be easy for a person skilled in the art to apply this known technique to the invention set forth in document 1 to provide the plurality of column selection lines YSW1 and YSW2 in such a manner as to correspond to groups into which a plurality of switches have been divided.

Moreover, a multi-bit pre-fetch type semiconductor storage device, wherein by activating only a sense amplifier with a burst length, burst length is increased without increasing current consumption, is known. See document 3 if necessary.

#### [Claims 2 and 5]

The inventions set forth in claims 2 and 5 do not involve an inventive step in the light of documents 1, 2 and 3 cited in the international search report.

As disclosed in document 3, for example, it is a known technique to divide a memory cell array into a plurality of blocks, and to selectively activate the

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Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

sense amplifier within a selected block.

[Claims 3 and 6]

The inventions set forth in claims 3 and 6 do not involve an inventive step in the light of documents 1, 2, 3 and 4 cited in the international search report.

As set forth in document 4, for example, it is a known technique to drive a column selection line in an asynchronous manner with an external clock, and it would not be particularly difficult for a person skilled in the art to drive a plurality of column selection lines in an asynchronous manner with an external clock.